	Type	Hits	Search Text	DBs	Time Stamp
1	BRS	16823	self?aligned adj contact adj plug or SAC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/13 22:09
2	BRS	3037	self?aligned adj contact	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/13 22:05
3	BRS	51	self?aligned adj contact adj plug or SAC adj plug	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/13 22:17
4	BRS	22	conductive with capping same (interlayer or interlevel) and spacer with capping and (interlayer or interlevel) with (etch etching etched) and (hole trench via opening aperture) with spacer and spacer with contact	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/13 22:15
5	BRS	292	contact adj plug with spacer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/24 13:25
6	BRS	6	self?aligned adj contact adj plug with spacer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/23 16:11
7	BRS	10103	contact adj plug	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/23 16:10
8	BRS	9	(self?aligned adj contact adj plug or SAC adj plug) with spacer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/13 22:17
9	BRS	40	self?aligned adj contact adj plug	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/13 22:23
10	BRS	18	contact adj plug same first adj spacer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/23 16:11
11	BRS	1	self?aligned adj contact adj plug same first adj spacer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/23 16:14

	Type	Hits	Search Text	DBs	Time Stamp
12	BRS	595	438/675.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/23 16:26
13	BRS	935	438/672.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/23 16:27
14	BRS	214	438/256.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/23 16:27
15	BRS	6	contact adj plug with spacer and spacer with plasma adj (etch etching etched)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/24 13:25
16	BRS	2	contact adj plug with spacer and spacer with plasma adj (etch etching etched) with (remove removing removed)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/24 13:40
17	BRS	1	kr-2003002110-$.did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2004/11/24 13:41

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Inventor Name Search Result

Your Search was:

Last Name = KIM
First Name = MYEONG-CHEOL

Application#	Patent#	Status	Date Filed	Title	Inventor Name 26
60601272	Not Issued	020	08/12/2004	MBMS AND CELL_ DCH STATE AND COUNTING PROCEDURE	KIM, MYEONG-CHEOL
60601267	Not Issued	020	08/12/2004	MBMS RECEPTION WHILST RECEIVING A DEDICATED SERVICE	KIM, MYEONG-CHEOL
60600244	Not Issued	020	08/09/2004	PRIORITISING OF DEDICATED SERVICE OVER MBMS RECEPTION	KIM, MYEONG-CHEOL
60599590	Not Issued	020	08/05/2004	PRIORITISING OF DEDICATED SERVICE OVER MBMS RECEPTION	KIM, MYEONG-CHEOL
60591436	Not Issued	020	07/26/2004	METHOD TO CHANGE THE SERVING RADIO NETWORK CONTROLLER FOR A UE SUPPORTING BROADCAST OR MULTICAST SERVICES	KIM, MYEONG-CHEOL
60589630	Not Issued	020	07/20/2004	SYSTEM AND METHOD OF SIGNALING IN MOBILE COMMUNICATION SYSTEM USING COMMON CONTROL CHANNEL	KIM, MYEONG-CHEOL
60581042	Not Issued	020	06/17/2004	MOBILE COMMUNICATION SYSTEM	KIM, MYEONG-CHEOL
60561943	Not Issued	020	04/12/2004	MAPPING OF MBMS SERVICE IDENTIFICATIONS	KIM, MYEONG-CHEOL
10867468	Not Issued	020	06/14/2004	INTEGRATED CIRCUIT SEMICONDUCTOR DEVICE WITH OVERLAY KEY AND ALIGNMENT KEY AND METHOD OF FABRICATING THE SAME	KIM, MYEONG-CHEOL

10858727	Not Issued	030	06/02/2004	SEMICONDUCTOR DEVICE WITH TRENCH GATE TYPE TRANSISTOR AND METHOD OF MANUFACTURING THE SAME	KIM, MYEONG-CHEOL
10802150	Not Issued	030	03/17/2004	ETCHING PROCESS INCLUDING PLASMA PRETREATMENT FOR GENERATING FLUORINE-FREE CARBON-CONTAINING POLYMER ON A PHOTORESIST PATTERN	KIM, MYEONG-CHEOL
10685032	Not Issued	030	10/14/2003	METHOD FOR CONTROLLING CD DURING AN ETCH PROCESS	KIM, MYEONG-CHEOL
10625027	Not Issued	071	07/22/2003	SEMICONDUCTOR DEVICE HAVING SELF-ALIGNED CONTACT PLUG AND METHOD FOR FABRICATING THE SAME	KIM, MYEONG-CHEOL
10410340	6784097	150	04/10/2003	METOD OF MANUFACTURING A SEMICONDUCTOR DEVICE WITH A SELF-ALIGNED CONTACT	KIM, MYEONG-CHEOL
10215309	6645879	150	08/08/2002	METHOD OF FORMING A SILICON OXIDE LAYER OF A SEMICONDUCTOR DEVICE AND METHOD OF FORMING A WIRING HAVING THE SAME	KIM, MYEONG-CHEOL
10193091	6573602	150	07/12/2002	SEMICONDUCTOR DEVICE WITH A SELF-ALIGNED CONTACT AND A METHOD OF MANUFACTURING THE SAME	KIM, MYEONG-CHEOL
10006199	6602789	150	12/04/2001	METHOD OF FORMING A METAL LINE IN A SEMICONDUCTOR MEMORY DEVICE	KIM, MYEONG-CHEOL
10001535	6682975	150	11/13/2001	SEMICONDUCTOR MEMORY DEVICE HAVING SELF-ALIGNED	KIM, MYEONG-CHEOL

				CONTACT AND FABRICATING METHOD THEREOF	
09933674	Not Issued	161	08/22/2001	SEMICONDUCTOR DEVICE WITH SELF-ALIGNED CONTACT STRUCTURE EMPLOYING DUAL SPACERS AND METHOD OF MANUFACTURING THE SAME	KIM, MYEONG-CHEOL
09878944	6690093	150	06/13/2001	METAL CONTACT STRUCTURE IN SEMICONDUCTOR DEVICE AND METHOD FOR FORMING THE SAME	KIM, MYEONG-CHEOL
09790240	Not Issued	090	02/21/2001	SEMICONDUCTOR MEMORY DEVICE HAVING SELF-ALIGNED CONTACTS AND METHOD OF FABRICATING THE SAME	KIM, MYEONG-CHEOL
09731385	Not Issued	071	12/06/2000	SEMICONDUCTOR DEVICE HAVING SELF-ALIGNED CONTACT AND FABRICATING METHOD THEREFOR	KIM, MYEONG-CHEOL
09656889	6451684	150	09/07/2000	SEMICONDUCTOR DEVICE HAVING A CONDUCTIVE LAYER SIDE SURFACE SLOPE WHICH IS AT LEAST 90 DEGREES AND METHOD FOR MANUFACTURING THE SAME	KIM, MYEONG-CHEOL
09654664	6573551	150	09/05/2000	SEMICONDUCTOR MEMORY DEVICE HAVING SELF-ALIGNED CONTACT AND FARICATING METHOD THEREOF	KIM, MYEONG-CHEOL
09487740	6385020	150	01/19/2000	METHODS OF FORMING HSG CAPACITORS FROM NONUNIFORMLY DOPED AMORPHOUS SILICON LAYERS AND HSG CAPACITORS FORMED	KIM, MYEONG-CHEOL

				THEREBY	
09359054	Not Issued	161	07/21/1999	SEMICONDUCTOR DEVICE HAVING A CONDUCTIVE LAYER SIDE SURFACE SLOPE WHICH IS AT LEAST 90 DEGREES AND METHOD FOR MANUFACTURING THE SAME	KIM , MYEONG-CHEOL

Inventor Search Completed: No Records to Display.

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Last Name KIM **First Name** MYEONG-CHEOL

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Inventor Name Search Result

Your Search was:

Last Name = KANG
First Name = CHANG-JIN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 20
10867468	Not Issued	020	06/14/2004	INTEGRATED CIRCUIT SEMICONDUCTOR DEVICE WITH OVERLAY KEY AND ALIGNMENT KEY AND METHOD OF FABRICATING THE SAME	KANG, CHANG-JIN
10858727	Not Issued	030	06/02/2004	SEMICONDUCTOR DEVICE WITH TRENCH GATE TYPE TRANSISTOR AND METHOD OF MANUFACTURING THE SAME	KANG, CHANG-JIN
10855165	Not Issued	092	05/27/2004	METHOD OF FABRICATING SEMICONDUCTOR DEVICE HAVING CAPACITOR	KANG, CHANG-JIN
10802150	Not Issued	030	03/17/2004	ETCHING PROCESS INCLUDING PLASMA PRETREATMENT FOR GENERATING FLUORINE-FREE CARBON-CONTAINING POLYMER ON A PHOTORESIST PATTERN	KANG, CHANG-JIN
10779494	Not Issued	030	02/13/2004	METHOD FOR FORMING WIRE LINE BY DAMASCENE PROCESS USING HARD MASK FORMED FROM CONTACTS	KANG, CHANG-JIN
10703947	Not Issued	030	11/04/2003	METHOD FOR ETCHING AN OBJECT USING A PLASMA AND AN OBJECT ETCHED BY A PLASMA	KANG, CHANG-JIN
10703328	Not Issued	030	11/07/2003	SEMICONDUCTOR DEVICE INCLUDING BIT LINE FORMED USING DAMASCENE TECHNIQUE	KANG, CHANG-JIN

				AND METHOD OF FABRICATING THE SAME	
10696417	Not Issued	030	10/29/2003	METHOD FOR FABRICATING A SEMICONDUCTOR DEVICE	KANG, CHANG-JIN
10652403	Not Issued	030	08/29/2003	METHOD OF OPTIMIZING SEASONING RECIPE FOR ETCH PROCESS	KANG, CHANG-JIN
10625027	Not Issued	071	07/22/2003	SEMICONDUCTOR DEVICE HAVING SELF-ALIGNED CONTACT PLUG AND METHOD FOR FABRICATING THE SAME	KANG, CHANG-JIN
10419075	Not Issued	030	04/21/2003	METHOD OF ETCHING A METAL LAYER USING A MASK, A METALLIZATION METHOD FOR A SEMICONDUCTOR DEVICE, A METHOD OF ETCHING A METAL LAYER, AND AN ETCHING GAS	KANG, CHANG-JIN
10322274	6753221	150	12/17/2002	METHODS FOR FABRICATING SEMICONDUCTOR DEVICES HAVING CAPACITORS	KANG, CHANG-JIN
10304273	6607954	150	11/26/2002	METHODS OF FABRICATING CYLINDER-TYPE CAPACITORS FOR SEMICONDUCTOR DEVICES USING A HARD MASK AND A MOLD LAYER	KANG, CHANG-JIN
10261595	Not Issued	030	09/30/2002	METHOD OF DRY-ETCHING SEMICONDUCTOR DEVICES	KANG, CHANG-JIN
09865585	Not Issued	161	05/29/2001	METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE USING OCTAFLUOROBUTENE ETCHING GAS AND SEMICONDUCTOR DEVICE MANUFACTURED THEREBY	KANG, CHANG-JIN
09229337	6143654	150	01/13/1999	METHOD OF FORMING TUNGSTEN PATTERN FOR A SEMICONDUCTOR DEVICE	KANG , CHANG-JIN
09226961	6177320	150	01/08/1999	METHOD FOR FORMING A SELF ALIGNED CONTACT IN A SEMICONDUCTOR DEVICE	KANG , CHANG-JIN

09203337	6169009	150	12/02/1998	METHODS OF ETCHING PLATINUM GROUP METAL FILM AND FORMING LOWER ELECTRODE OF CAPACITOR	KANG , CHANG-JIN
09190383	Not Issued	161	11/10/1998	METHOD FOR FORMING CONTACT OF SEMICONDUCTOR DEVICE	KANG , CHANG-JIN
09008525	Not Issued	041	06/29/1998	SEMICONDUCTOR MEMORY DEVICE AND METHOD OF MANUFACTURING FOR PREVENTING BIT LINE OXIDATION	KANG, CHANG-JIN

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Last Name KANG **First Name** CHANG-JIN

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Inventor Name Search Result

Your Search was:

Last Name = CHI
First Name = KYEONG-KOO

Application#	Patent#	Status	Date Filed	Title	Inventor Name 30
10864428	Not Issued	020	06/10/2004	WAFER STAGE INCLUDING ELECTROSTATIC CHUCK AND METHOD FOR DECHUCKING WAFER USING THE WAFER STAGE	CHI, KYEONG-KOO
10858727	Not Issued	030	06/02/2004	SEMICONDUCTOR DEVICE WITH TRENCH GATE TYPE TRANSISTOR AND METHOD OF MANUFACTURING THE SAME	CHI, KYEONG-KOO
10855165	Not Issued	092	05/27/2004	METHOD OF FABRICATING SEMICONDUCTOR DEVICE HAVING CAPACITOR	CHI, KYEONG-KOO
10802150	Not Issued	030	03/17/2004	ETCHING PROCESS INCLUDING PLASMA PRETREATMENT FOR GENERATING FLUORINE-FREE CARBON-CONTAINING POLYMER ON A PHOTORESIST PATTERN	CHI, KYEONG-KOO
10779494	Not Issued	030	02/13/2004	METHOD FOR FORMING WIRE LINE BY DAMASCENE PROCESS USING HARD MASK FORMED FROM CONTACTS	CHI, KYEONG-KOO
10703947	Not Issued	030	11/04/2003	METHOD FOR ETCHING AN OBJECT USING A PLASMA AND AN OBJECT ETCHED BY A PLASMA	CHI, KYEONG-KOO
10703328	Not Issued	030	11/07/2003	SEMICONDUCTOR DEVICE INCLUDING BIT LINE FORMED USING	CHI, KYEONG-KOO

				DAMASCENE TECHNIQUE AND METHOD OF FABRICATING THE SAME	
10652403	Not Issued	030	08/29/2003	METHOD OF OPTIMIZING SEASONING RECIPE FOR ETCH PROCESS	CHI, KYEONG-KOO
10625027	Not Issued	071	07/22/2003	SEMICONDUCTOR DEVICE HAVING SELF-ALIGNED CONTACT PLUG AND METHOD FOR FABRICATING THE SAME	CHI, KYEONG-KOO
10413372	Not Issued	095	04/15/2003	DRAM CELL	CHI, KYEONG-KOO
10364344	Not Issued	030	02/12/2003	SEMICONDUCTOR ETCHING APPARATUS AND METHOD OF ETCHING SEMICONDUCTOR DEVICES USING SAME	CHI, KYEONG-KOO
10304273	6607954	150	11/26/2002	METHODS OF FABRICATING CYLINDER-TYPE CAPACITORS FOR SEMICONDUCTOR DEVICES USING A HARD MASK AND A MOLD LAYER	CHI, KYEONG-KOO
10038911	6570205	150	01/08/2002	DRAM CELL	CHI, KYEONG-KOO
09998928	Not Issued	161	12/03/2001	METHOD OF MANUFACTURING A SELF-ALIGNED CONTACT FROM A CONDUCTIVE LAYER THAT IS FREE OF VOIDS	CHI, KYEONG-KOO
09983790	6767834	150	10/25/2001	METHOD OF MANUFACTURING A CONTACT OF A SEMICONDUCTOR DEVICE USING CLUSTER APPARATUS HAVING AT LEAST ONE PLASMA PRETREATMENT MODULE	CHI, KYEONG-KOO
09951709	6793767	150	09/14/2001	WAFER STAGE INCLUDING ELECTROSTATIC CHUCK AND METHOD FOR DECHUCKING WAFER USING THE WAFER STAGE	CHI, KYEONG-KOO

09916319	6562651	150	07/30/2001	METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE HAVING CONTACT PADS	CHI, KYEONG-KOO
09832426	Not Issued	161	04/10/2001	PROCESS AND APPARATUS FOR DRY-ETCHING A SEMICONDUCTOR LAYER	CHI, KYEONG-KOO
09793143	Not Issued	161	02/27/2001	SEMICONDUCTOR ETCHING APPARATUS AND METHOD OF ETCHING SEMICONDUCTOR DEVICES USING SAME	CHI, KYEONG-KOO
09760818	6498081	150	01/17/2001	METHOD OF MANUFACTURING SELF-ALIGNED CONTACT HOLE	CHI, KYEONG-KOO
09708737	6534921	150	11/09/2000	METHOD FOR REMOVING RESIDUAL METAL-CONTAINING POLYMER MATERIAL AND ION IMPLANTED PHOTORESIST IN ATMOSPHERIC DOWNSTREAM PLASMA JET SYSTEM	CHI, KYEONG-KOO
09104463	Not Issued	161	06/24/1998	APPARATUS FOR DRY ETCHING A SEMICONDUCTOR LAYER WITH PHASE MODULATED RF POWER SUPPLIES	CHI , KYEONG-KOO
09063040	Not Issued	168	04/21/1998	PLASMA DIFFUSION CONTROL METHOD AND APPARATUS THEREOF	CHI , KYEONG-KOO
08885336	5914276	150	06/30/1997	METHODS OF FORMING ELECTRICALY CONDUCTIVE LINES USING NITROGEN AND CHLORINE CONTAINING GAS MIXTURES	CHI , KYEONG-KOO
08857754	6159811	150	05/15/1997	METHOD FOR PATTERNING MICROELECTRONIC STRUCTURES USING CHLORINE, OXYGEN AND FLUORINE	CHI , KYEONG-KOO
08818256	5859501	150	03/14/1997	RADIO FREQUENCY GENERATING SYSTEMS	CHI , KYEONG-KOO

				AND METHODS FOR FORMING PULSE PLASMA USING GRADUALLY PULSED TIME-MODULATED RADIO FREQUENCY POWER	
08782305	6087264	150	01/15/1997	METHODS FOR PATTERNING MICROELECTRONIC STRUCTURES USING CHLORINE AND OXYGEN	CHI , KYEONG-KOO
08740670	5808733	150	02/24/1997	DEVICE FOR MEASURING FEEBLE LIGHT AND METHOD THEREOF	CHI , KYEONG-KOO
08731955	5932492	150	10/23/1996	METHODS FOR FORMING ALUMINA MASKING	CHI , KYEONG-KOO
08698362	5772772	150	08/15/1996	PLASMA DIFFUSION CONTROL APPARATUS	CHI , KYEONG-KOO

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Inventor Name Search Result

Your Search was:

Last Name = SON
First Name = SEUNG-YOUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name 7
10855165	Not Issued	092	05/27/2004	METHOD OF FABRICATING SEMICONDUCTOR DEVICE HAVING CAPACITOR	SON, SEUNG-YOUNG
10830941	Not Issued	030	04/22/2004	METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE WITH INTERCONNECTIONS AND INTERCONNECTION CONTACTS AND A DEVICE FORMED THEREBY	SON, SEUNG-YOUNG
10703328	Not Issued	030	11/07/2003	SEMICONDUCTOR DEVICE INCLUDING BIT LINE FORMED USING DAMASCENE TECHNIQUE AND METHOD OF FABRICATING THE SAME	SON, SEUNG-YOUNG
10625027	Not Issued	071	07/22/2003	SEMICONDUCTOR DEVICE HAVING SELF-ALIGNED CONTACT PLUG AND METHOD FOR FABRICATING THE SAME	SON, SEUNG-YOUNG
10419075	Not Issued	030	04/21/2003	METHOD OF ETCHING A METAL LAYER USING A MASK, A METALLIZATION METHOD FOR A SEMICONDUCTOR DEVICE, A METHOD OF ETCHING A METAL LAYER, AND AN ETCHING GAS	SON, SEUNG-YOUNG
[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]

10322274	6755221	150	12/17/2002	METHODS FOR FABRICATING SEMICONDUCTOR DEVICES HAVING CAPACITORS	SON, SEUNG-YOUNG
10172010	Not Issued	030	06/17/2002	WASHING MACHINE WITH AN IMPROVED WATER TUB	SONG, SEUNG-YOUNG

Inventor Search Completed: No Records to Display.

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